

FIG. 1

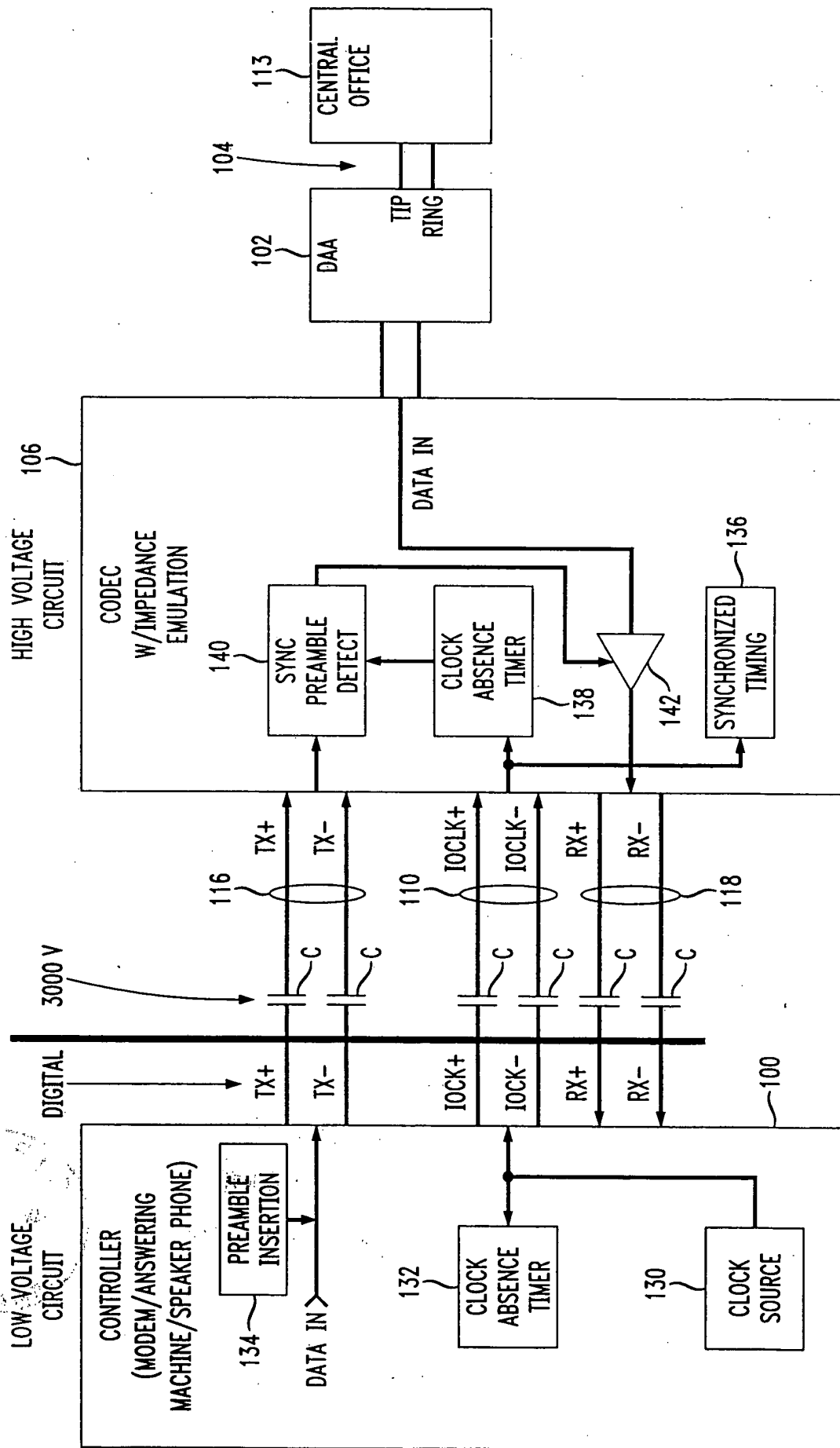


FIG. 3

PRIOR ART

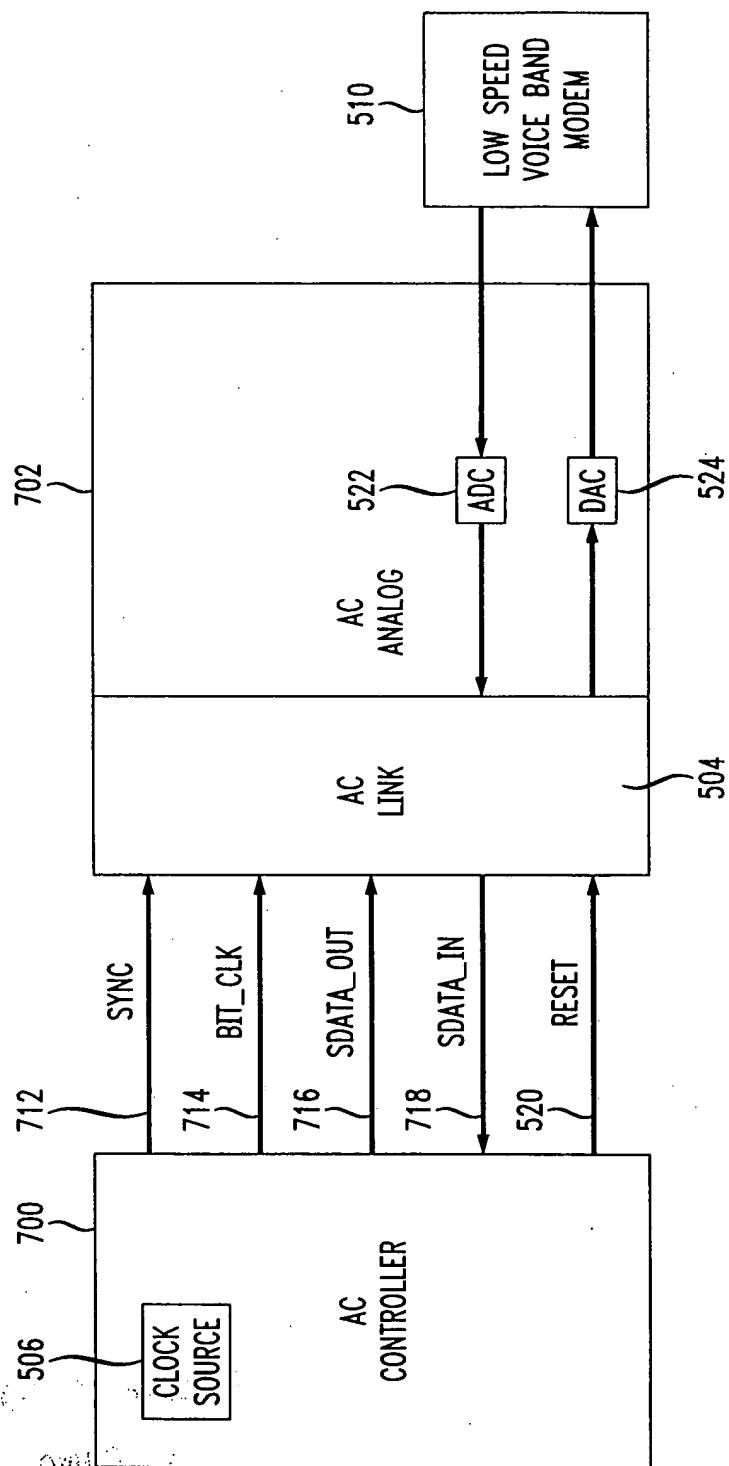


FIG. 4

PRIOR ART

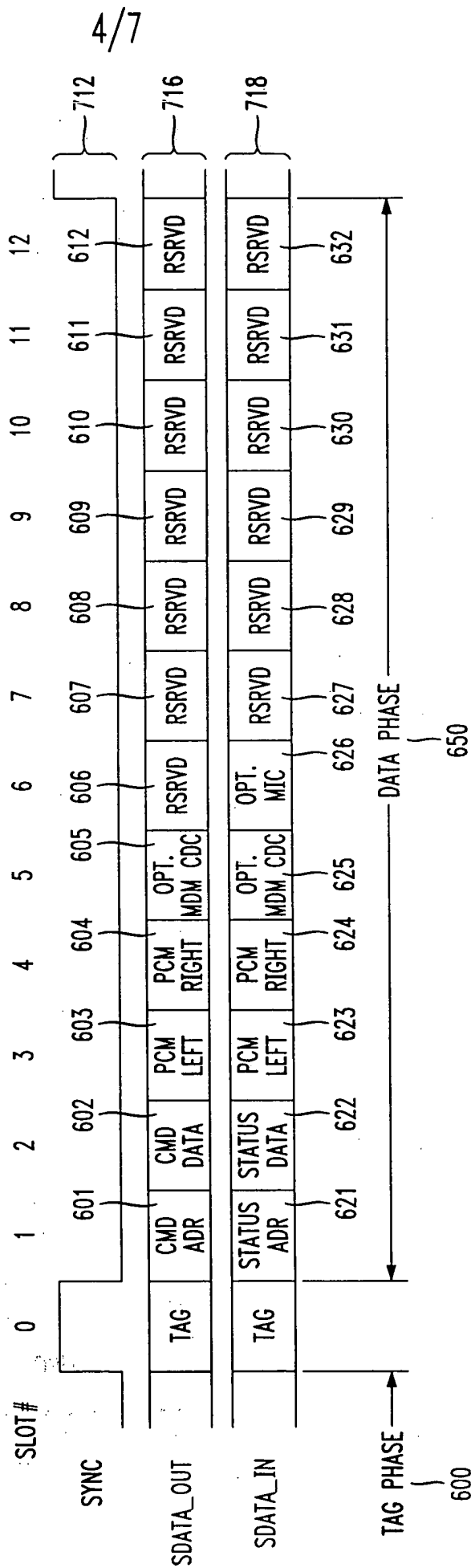


FIG. 5

PRIOR ART

AC-LINK AUDIO OUTPUT FRAME

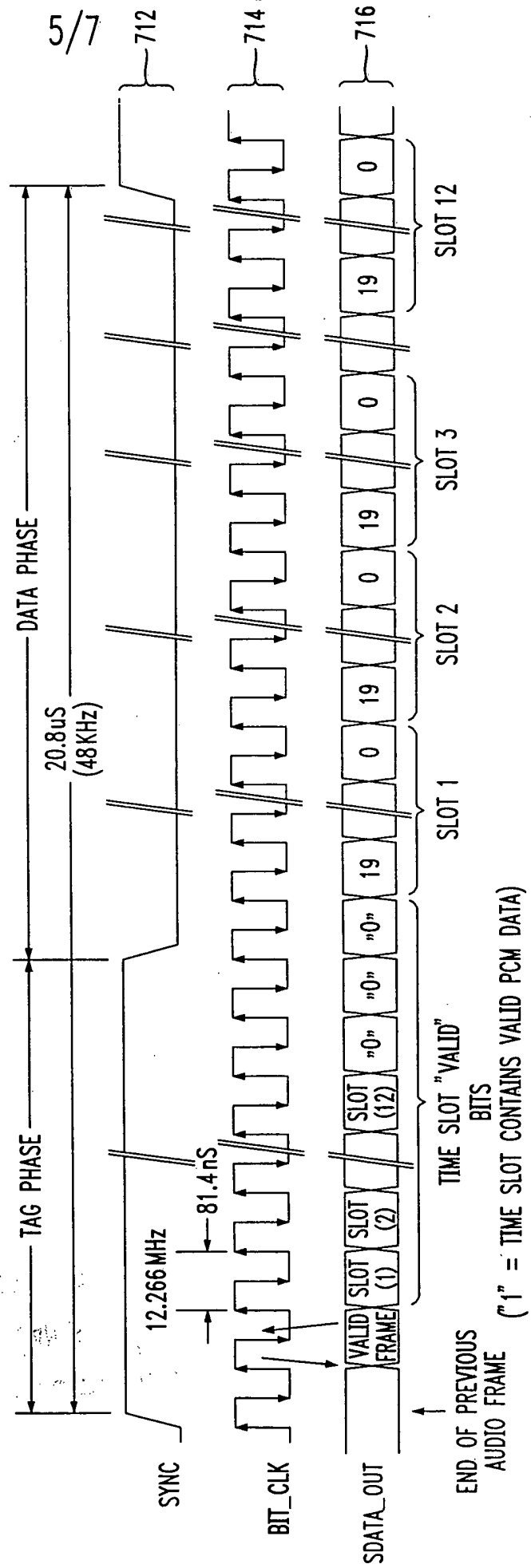


FIG. 6
PRIOR ART

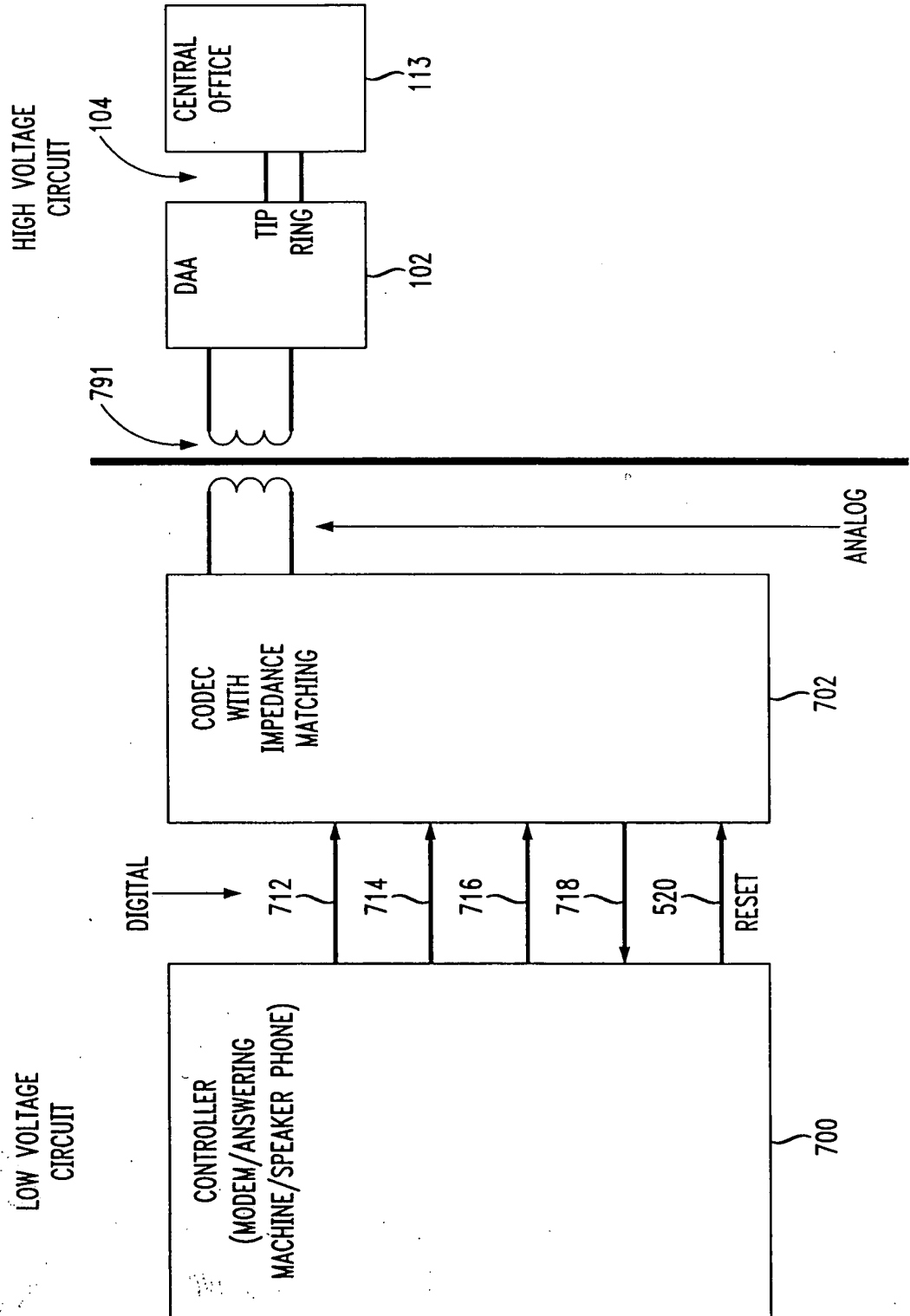
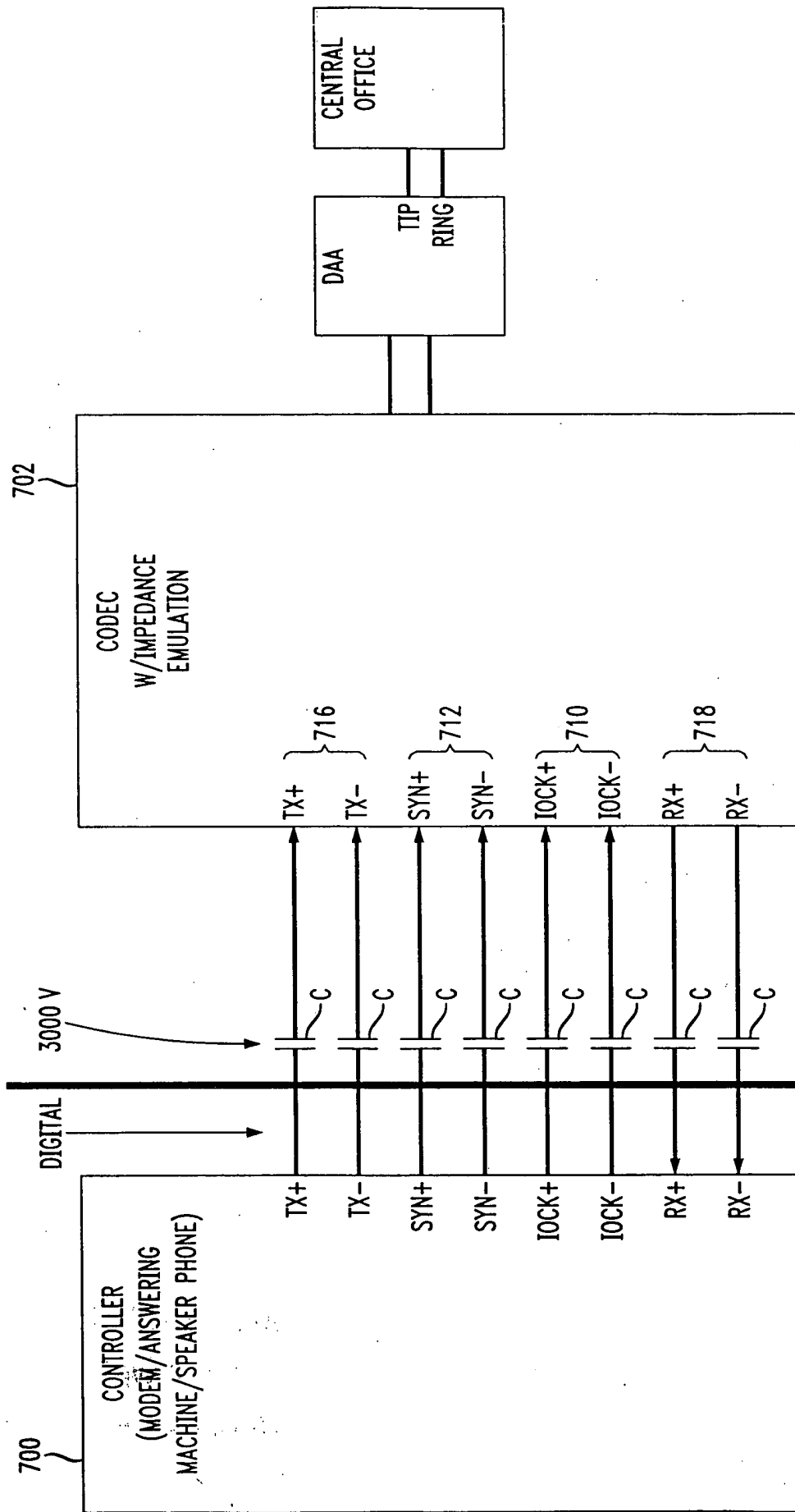


FIG. 7
PRIOR ART

LOW VOLTAGE
CIRCUIT

HIGH VOLTAGE
CIRCUIT



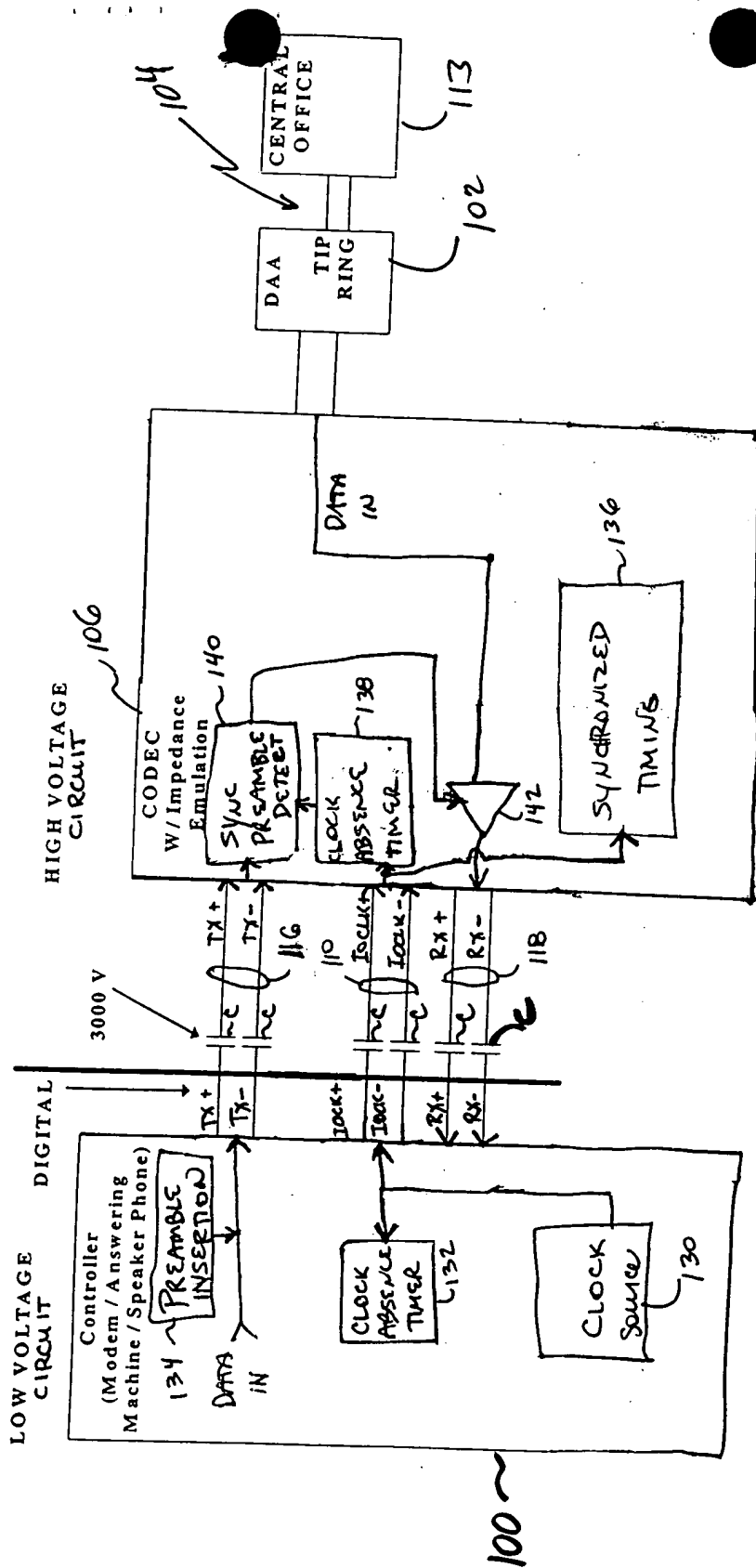


Fig. 1

Block diagram of a SYNC preamble detection circuit. The circuit is enclosed in a dashed box labeled "SYNC PREAMBLE DETECT".

Inputs:

- DATA IN**: Splits into two paths. One path goes through a 16 Bits Data register (208) and an 8 Bits Address (Up to 32 Registers) register (206) to output **DATA[0:15]** and **ADR[0:7]** to registers.
- ILOCK**: Resets the **CLOCK ABSENCE TIMER** (138) and is also input to the 16 Bits Data register (208).

Internal Components and Signals:

- CLOCK ABSENCE TIMER** (138): Outputs **RESET** to the 16 Bits Data register (208).
- COMPARE AND LATCH** (204): Receives **16 Bits Data** (210) and **8 Bits Address (Up to 32 Registers)** (210b) from the 16 Bits Data register (208). It also receives a **Set Preamble** value (202, ex 11000110) and outputs **8 Bits Preamble** (210e) to the 8 Bits Address (Up to 32 Registers) register (210c).
- CODEC ON BIT** (143): Output from the **COMPARE AND LATCH** (204) to an inverter (142), which outputs **Sigma Delta Data To Codec**.

Registers and Buffers:

- 16 Bits Data** (208): Receives **DATA IN** and **RESET** from the **CLOCK ABSENCE TIMER** (138).
- 8 Bits Address (Up to 32 Registers)** (206): Receives **DATA IN** and **8 Bits Preamble** (210e) from the **COMPARE AND LATCH** (204).
- 16 Bits Data** (210): Receives **DATA IN** and **RESET** from the **CLOCK ABSENCE TIMER** (138).
- 8 Bits Address (Up to 32 Registers)** (210b): Receives **DATA IN** and **8 Bits Preamble** (210e) from the **COMPARE AND LATCH** (204).
- 8 Bits Preamble** (210e): Output from the **COMPARE AND LATCH** (204) to the 8 Bits Address (Up to 32 Registers) register (210c).

FIG. 2

FIG. 3

PRIOR ART

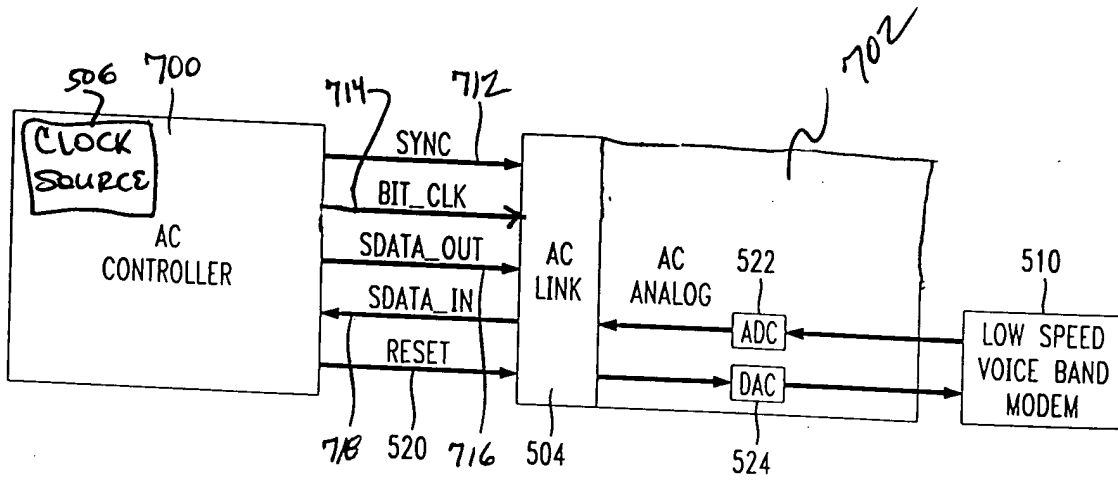


FIG. 4

PRIOR ART

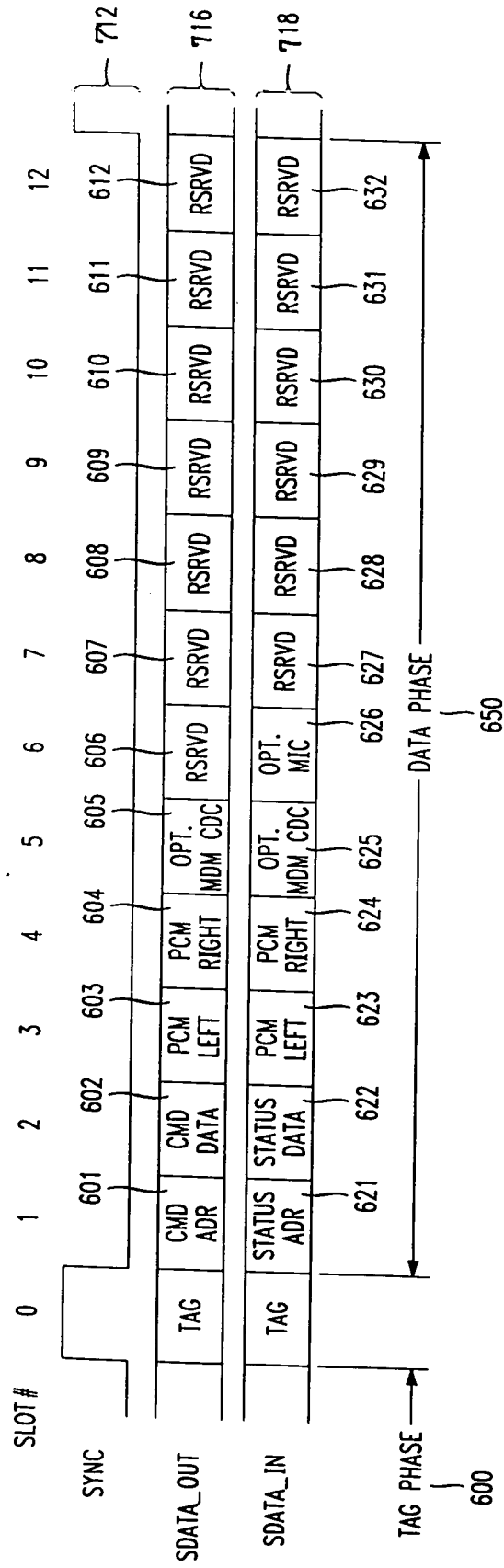
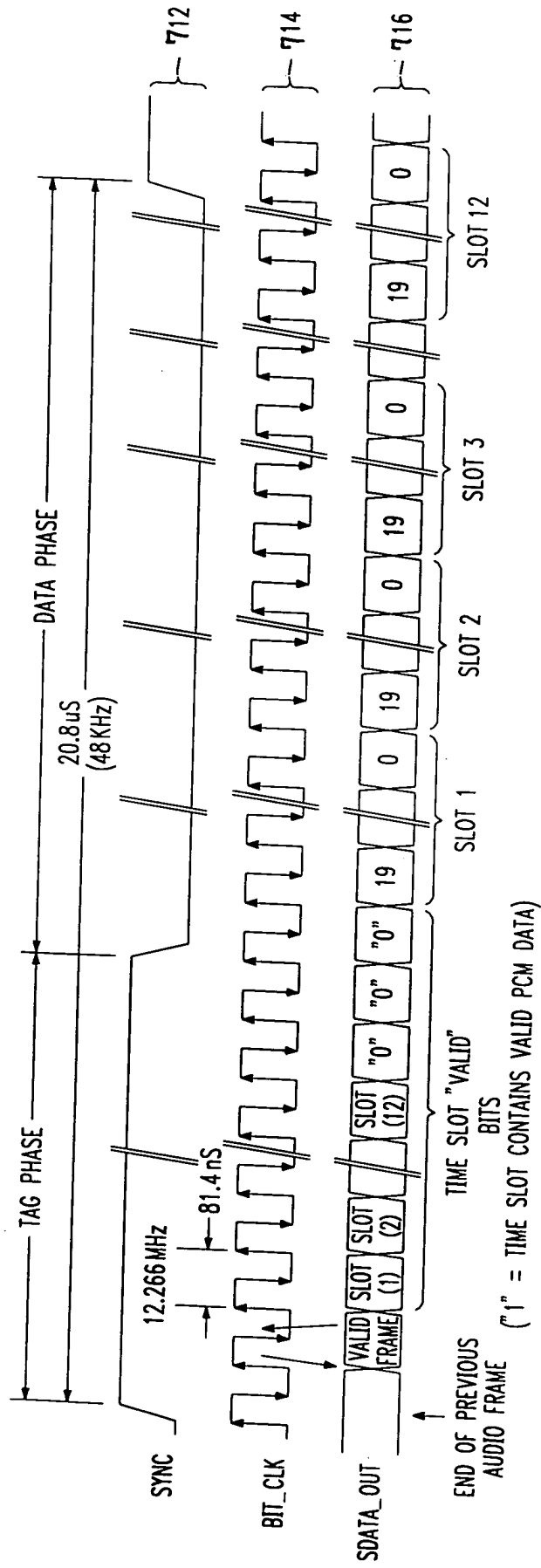


FIG. 5

PRIOR ART

AC-LINK AUDIO OUTPUT FRAME



LOW VOLTAGE
CIRCUIT

DIGITAL

Controller
(Modem / Answering
Machine / Speaker Phone)

712
714
716
718
510
mut

CODEC
With
Impedance
Matching

ANALOG

HIGH VOLTAGE
CIRCUIT

DAA
TIP
RING

CENTRAL
OFFICE

FIG. 6

PRIOR ART

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SECRET

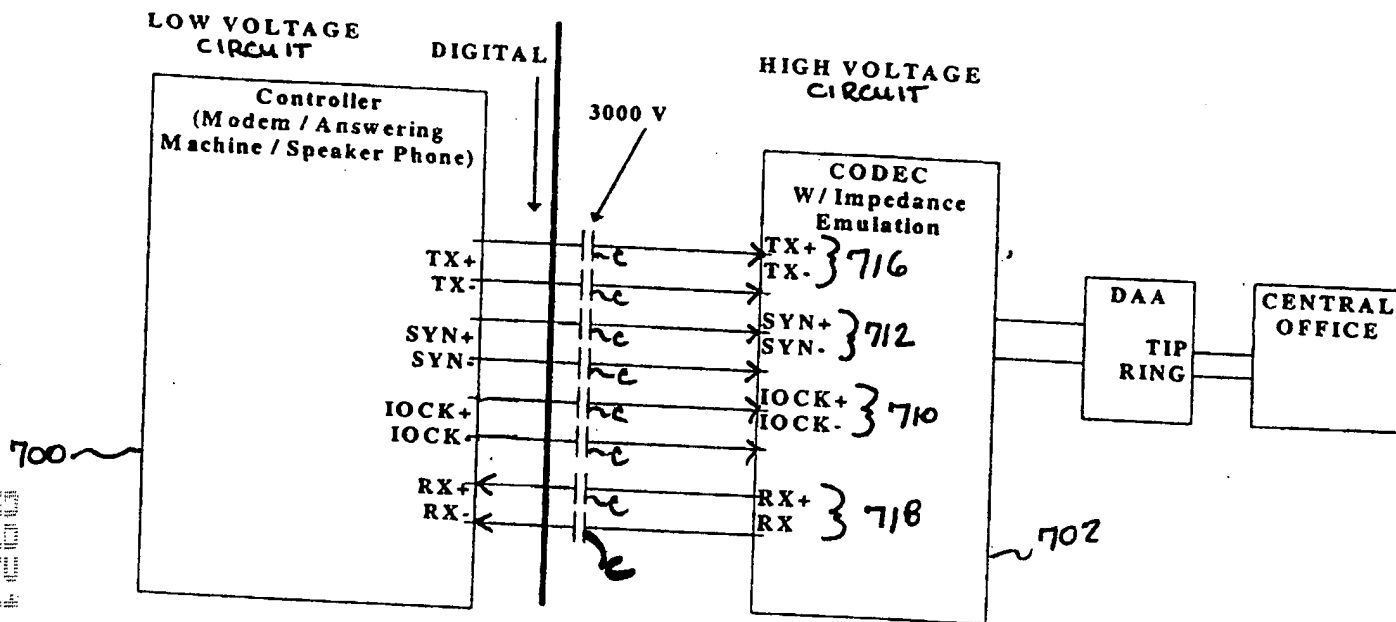


FIG. 7

PRIOR ART